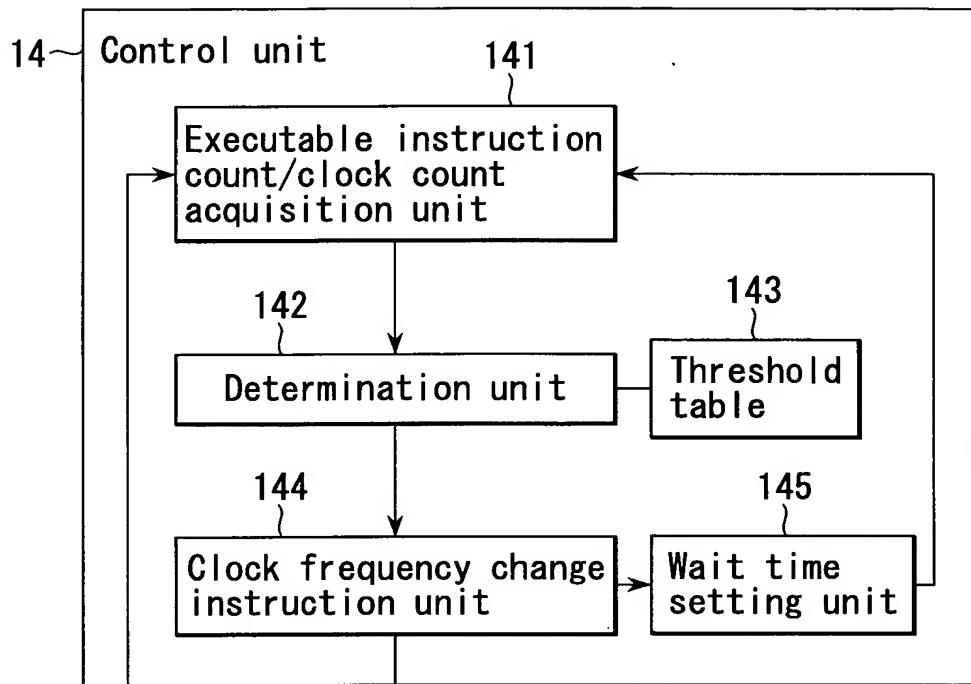


FIG. 1



- Executable instruction count per unit time
- Clock count per unit time (clock frequency)

From CPU (including register and clock processing circuit) To CPU (including clock processing circuit)

Clock frequency change instruction

FIG. 2

S	Mode type
0.9	Power saving mode
0.5	Standard mode
0.1	High-speed mode

FIG. 3

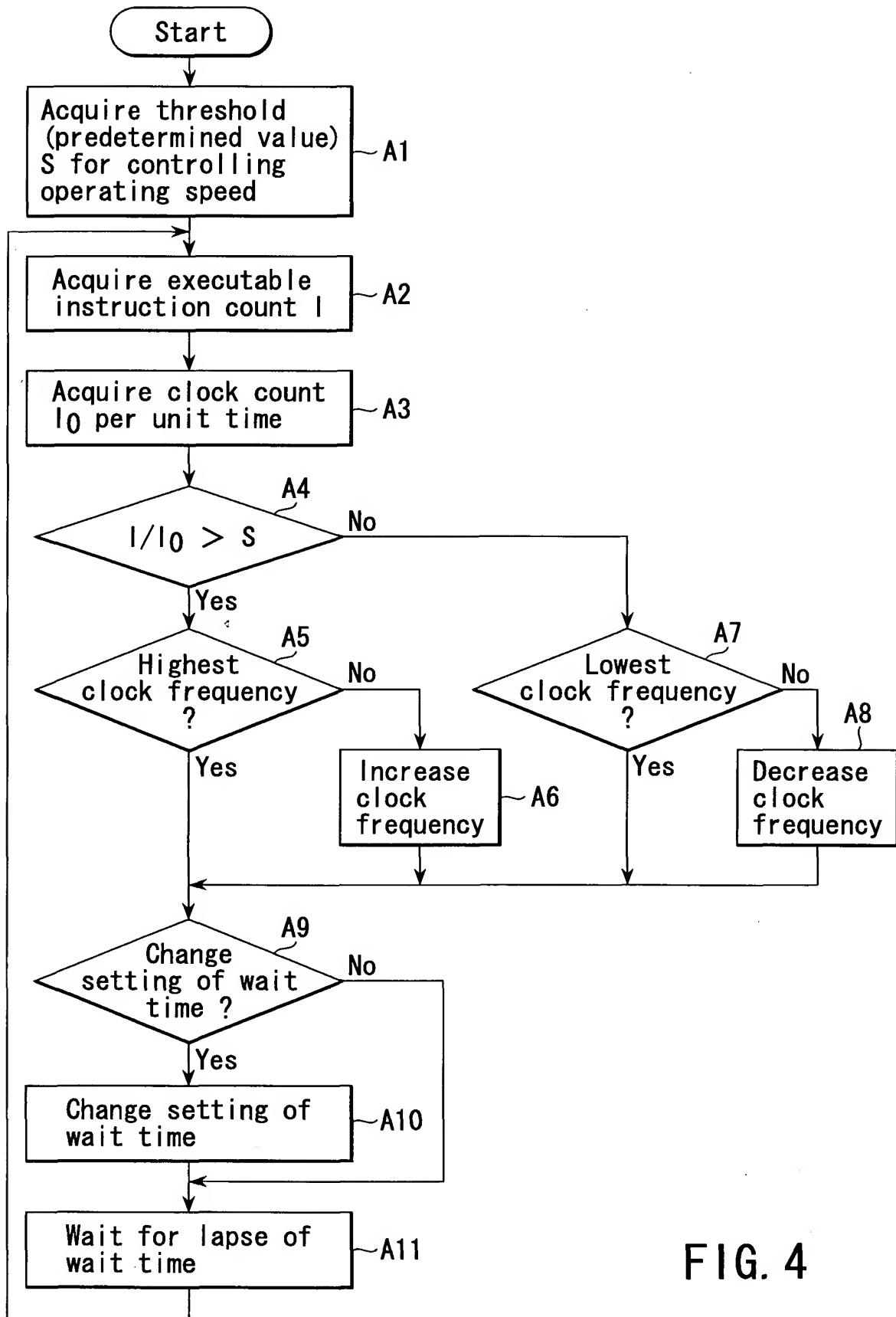


FIG. 4

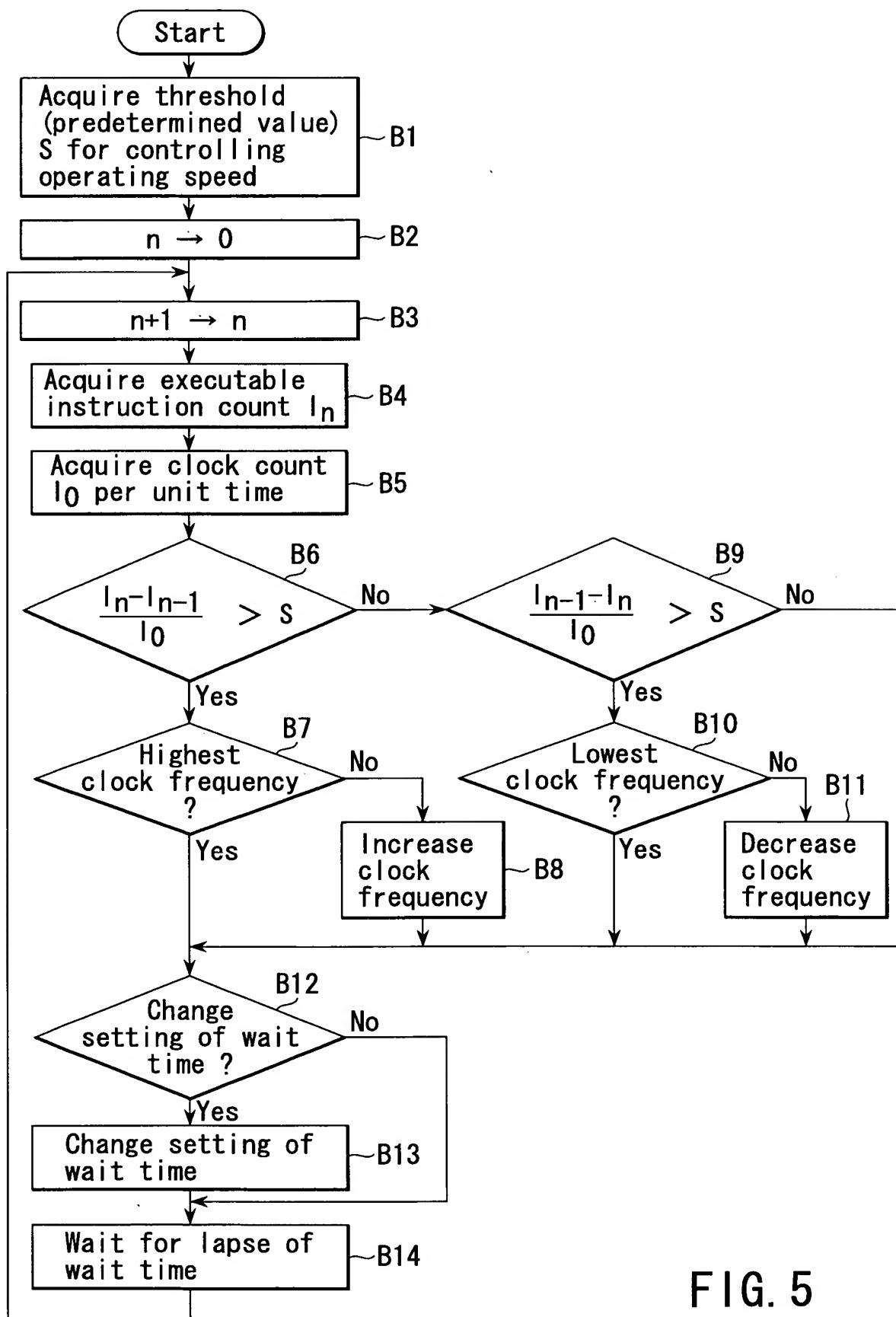


FIG. 5

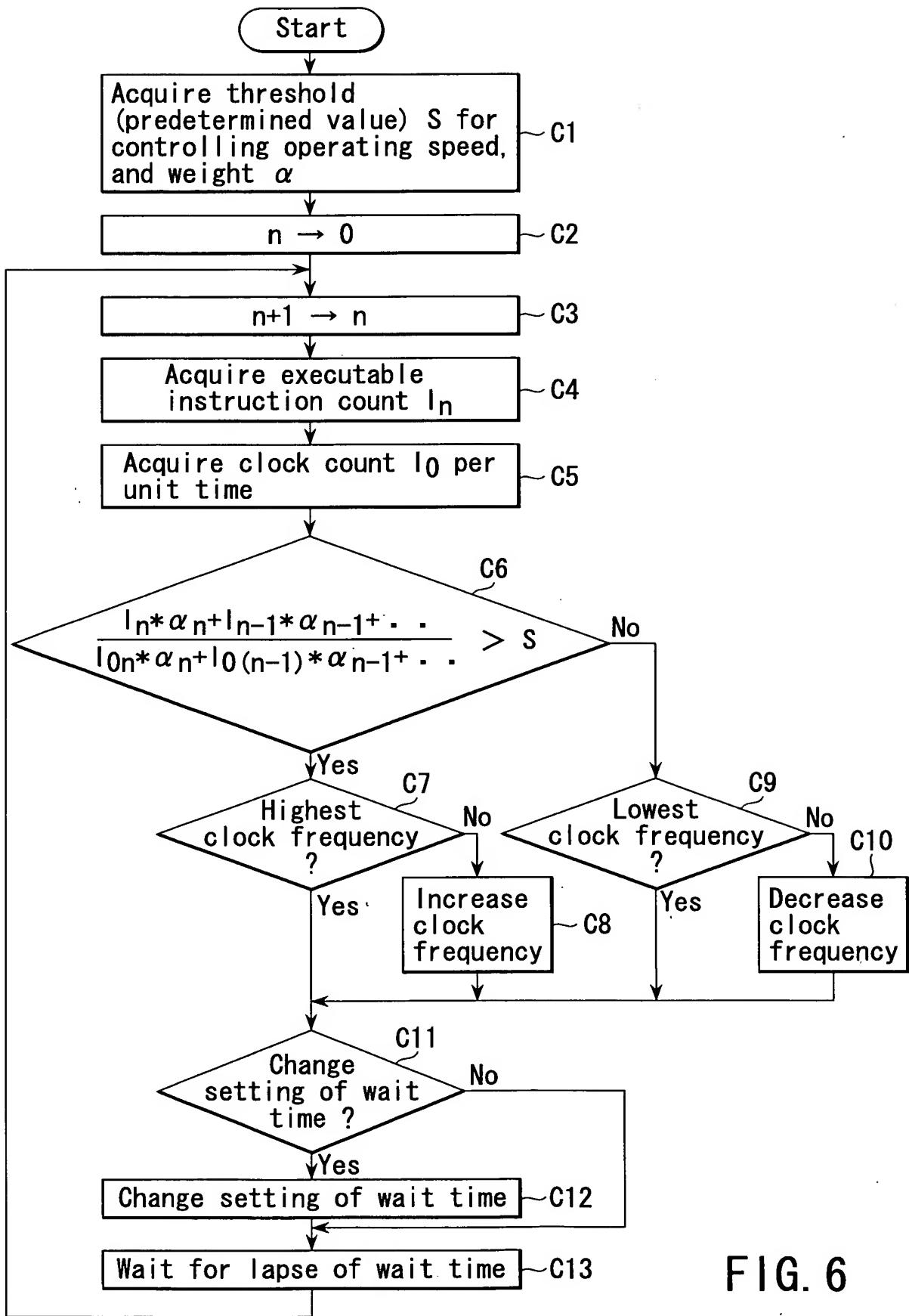


FIG. 6